

New FPGA processing code for JET gamma-ray camera upgrade

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The Gamma Ray Camera Upgrade (GCU) project aims at installing a new set of 19 scintillators with multi-pixel photon counter (MPPC) embedded, capable to meet the high fluxes expected during deuterium-tritium plasmas while improving the diagnostic spectroscopic capabilities. GCU will benefit from the Advanced Telecommunications Computing Architecture (ATCA) based Data Acquisition System (DAQ), successfully installed and commissioned during the JET-EP2 enhancement. However, to cope with the new GCU detector signals, the DAQ Field Programmable Gate Array (FPGA) codes need to be rebuilt. This work presents the FPGA code upgrade for DAQ gamma camera, capable to sustain the expected fast response of new detectors, while exploiting the full capabilities of the DAQ. Dedicated codes were designed, capable to acquire the new signals at full rate, and simultaneously processing it in real-time through suitable algorithms, fitted to the new signal shape. First results of real-time processing codes applied to data from detector prototypes are presented.

Keywords: ATCA, FPGA, Real-Time Processing, Gamma-ray Spectroscopy, Nuclear Fusion.

1. Introduction

The 2D Gamma-ray Camera of the Joint European Torus (JET) is one of the target diagnostics for physics exploitation during next high-power Deuterium-Tritium (DT) experiments [1]. From the gamma-ray emission spectra, associated with specific reactions among fast ions and fusion alphas with impurities, it will be possible to infer information on the spatial distribution of these fast ions and alpha particles, and to follow their evolution in time [1].

The Gamma-ray Camera Upgrade (GCU) project aims to improve the spectroscopic properties of the existing gamma camera in terms of energy resolution and high counting rate capability, allowing its operation during DT experiments [2, 3]. New 19 LaBr₃ based detectors were installed during 2017 shutdown, featuring an energy resolution of 5% (Energy Resolution = FWHM/En) at 1.1 MeV and count rate capability in excess of 500 kHz [3, 4, 5].

The GCU Data Acquisition (DAQ) system, an Advanced Telecommunications Computing Architecture (ATCA) based DAQ successfully installed and commissioned during the JET-EP2 enhancement [6], aims

to acquire and simultaneously process the new 19 GCU detector signals. Dedicated algorithms for DAQ Field Programmable Gate Arrays (FPGAs) are being developed, capable of real-time processing the incoming signals, delivering only the energy value of founded events and its corresponding time occurrence. During past experiments the DAQ was operating at a down-sampling rate of 2.5 Msamples/s, coping with the long pulse length of former CsI(Tl) based detectors [6]. As so, the new algorithms are needed to properly process the new fast signals at full DAQ rate, as described in next sections.

2. GCU DAQ

The GCU DAQ system is composed by an ATCA shelf with 3 digitizer modules connected to a controller through PCI-express (PCIe) links [7]. Each ATCA digitizer module is composed by eight Analog to Digital Converters (ADC) with a maximum sampling rate of 250 Msamples/s, 2 GB of local memory (DDR2) and two Virtex-4 FPGAs [8]. Besides the basic module functionalities, the true parallelism of FPGAs make them suitable for real-time processing and data reduction. Four data management operation modes were designed for

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GCU FPGAs: (i) raw data – all acquired samples are stored in local memory; (ii) pulse storage - occurrences above a predefined threshold are stored; (iii) processing - perform real time algorithms storing only the energy of the events with its corresponding time stamp; (iv) filter – when a processing filter is present (e.g. trapezoidal shaper [9]), this option stores filtered data for calibration and debug. For all modes, stored data is sent through DMA packets to host when the acquisition finishes.

From the maximum count rate expected during DT (500kevents/channel) [3, 4], and according with the total digitizer memory available per ADC channel (500 MB) for each JET discharge, it was concluded, as depicted in table 1, that real-time processing is needed for data storage without losses at those rates. Furthermore, this was the default mode of past experiments, allowing proper inter-shot pulse analyses of all 19 CsI(Tl) signals during experiments.

Table 1. Memory requested per channel for a single discharge.

	Count Rate (kevent/s)	Samples /event*	Time @ max count rate (s)	Mem. /channel (MB)
Pulse storage	500	64	10	680
	500	128	10	1320
RT process	500	4	10	80
	1000	4	30	480

*1 sample = 2-Byte

According with figs. 1 and 2, events from LaBr₃ based detector (fig. 1) present a much shorter pulse length and a slightly different shape, when compared with former CsI(Tl) signals (fig. 2).

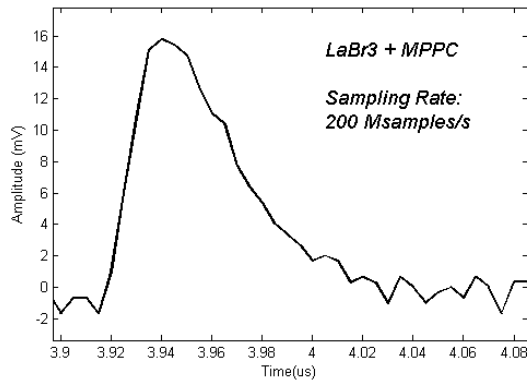


Fig. 1. Pulse from LaBr₃, after its installation in gamma camera, at lab, during shutdown (according with past experience, additional noise is foreseen with camera in place during experiments).

The events from former CsI(Tl) based detectors (fig.2), in operation until end of 2016, presented pulse lengths >400 μs, and shape similar to ramp-like pulses. Dedicated algorithms were implemented at FPGA in order to properly process these type of signals [10].

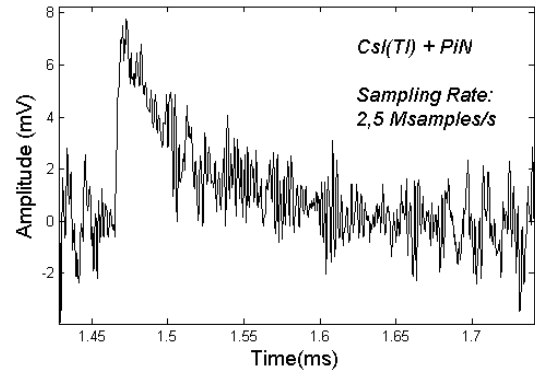


Fig. 2. Pulse from CsI(Tl) based detectors, with cameras in place during experiments.

In opposite, LaBr₃ based detectors deliver short pulses with sharp peaks and fast decay (~120 ns pulse length), similar to exponentials (fig.1). As so, new algorithms were identified and implemented at GCU FPGAs, capable to correctly process the fast LaBr₃ incoming signals.

3. Real-time processing code

Different methods can be used for real-time processing GCU detector signals. Foreseeing its portability to an FPGA environment, three algorithms were selected and tested: i) Pulse Height Analysis (PHA) – the pulses amplitude is proportional to the energy of the corresponding events interacting in the detector; ii) Digital Trapezoidal Shaper (DTS) – exponential signals are transformed into trapezoids, whose amplitude is proportional to the energy of the events; Charge Integration (CI) – The pulse area is proportional to the energy of the events.

Tests were made by post-processing the data, from a detector prototype in presence of high rates (up to 1,5 Mevents/s), acquired at TANDEM-ALPI accelerator of the Legnaro National Laboratories. Expected spectra with multi gamma lines can be observed with that data, from reactions between 10 MeV proton and a ²⁷Al target placed in a cylindrical vacuum chamber [3, 5].

Taking into account the rates expected during DT, it was concluded that the CI method presents the highest dependence on count rate, with special spectra degradation at high rates. In opposite, both PHA and DTS algorithms are able to produce expected spectra even at high count rates (1,5 Mevents/s), with improved peaks resolution when DTS method is applied. The latest, considered as a quasi-optimum filter capable of suppressing ballistic deficit of sharp exponential pulses [9], is being widely used at improved spectroscopy analyses. As so, DTS is considered so far the most adequate method for real-time processing the GCU data. However, this method requires adjustments on filter parameters, dependent on signal shape and noise, as so less immediate than PHA. The PHA is considered the fastest method. It can easily determine the energy of the pulses, very useful for fast results during test phases (e.g. detectors installation; first tests when camera in place).

Both PHA and DTS based algorithms were implemented in GCU FPGAs. Taking into account the expected pile-up during DT, the DTS parameters were slightly modified. As depicted in figure 3, filtered signals are similar to a Gaussians instead of pure trapezoids.

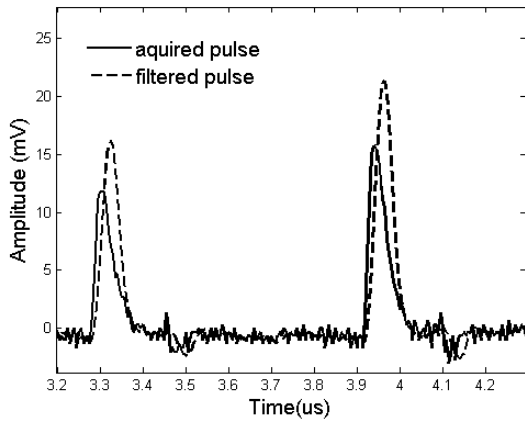


Fig. 3. Raw data pulses and filtered pulses based on DTS.

4. Results

The two real-time methods, PHA and DTS, were successfully implemented and tested with prototype detectors. First results were obtained from the PHA method applied to the prototype detector (first version) installed in channel #10 of gamma camera during 2016 experiments. In figure 4 is depicted the time trace (counts/time) obtained for discharge #91975, highlighting the interval when the two heating systems, the neutral beam injector (NBI) and the Ion cyclotron resonance heating (ICRH), were launched.

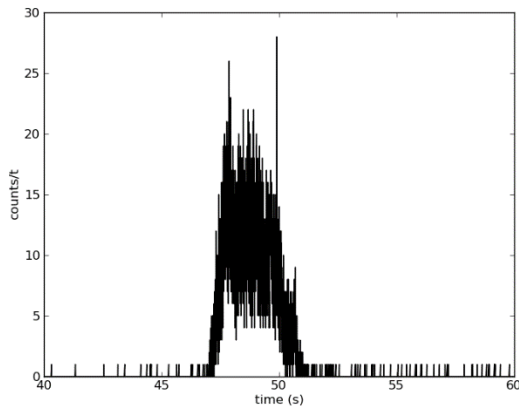


Fig. 4. Time trace of detector #10 in horizontal camera (first version of GCU prototype detectors) during discharge #91975. It was obtained from real-time data processed at FPGA using the PHA method.

For the detectors installation phase (final version), both horizontal and vertical cameras were temporally placed at laboratory, near diagnostic cubicle. Single acquisition were made for all detectors, through a long cable (~10 m) connecting each detector to an ATCA ADC channel. Were made short (120 s) and long (up to 6 h)

acquisitions, in presence of ^{22}Na and ^{137}Cs radioactive sources. Due to security reasons, it was not allowed to use stronger sources with high energy peaks.

During the installation tests the DTS method was also used, being possible to produce suitable spectra for all 19 channels with this improved solution. As example, figure 3 depicts a spectrum of data from channel 10, produced with DTS method at FPGA, for 6 hours acquisition, in presence of ^{22}Na radioactive source (week source - ~ 14 events/s - embedded in detectors set). Figure 4 presents a spectrum of data produced with the same method, from the same detector, for 3 minutes acquisition, in presence of ^{137}Cs radioactive source (high activity source ~ 1.8 kevents/s).

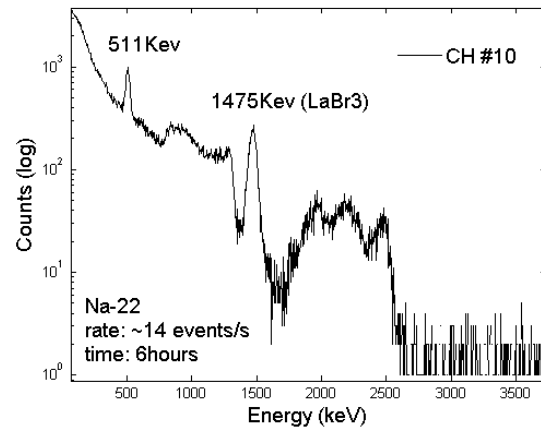


Fig. 5. Spectrum of channel 10, for 6 hours acquisition using the DTS method at FPGA, in presence of ^{22}Na radioactive source (week source - ~ 14events/s - embedded in detectors set).

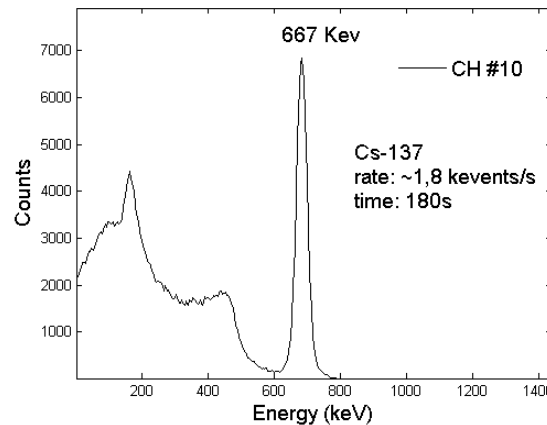


Fig. 6. Spectrum of channel 10 for 3 minutes acquisition using the real-time DTS algorithm at FPGA, in presence of ^{137}Cs radioactive source (~1.8 kevents/s).

Similar spectra were observed for all 19 channels, as expected. Concerning the acquisitions in presence of ^{137}Cs source, it was achieved an energy resolution (FWHM/E) between 4.7% and 5.7% for the 667 keV peak, depending on the detector, which is in agreement with detector datasheets [11].

Moreover, real-time spectra were validated with spectra obtained from post-processing methods using

event based data, as well as with spectra produced with a CAEN DTS5730 portable digitizer card [12].

5. Conclusions

This paper presents the first results obtained with two selected algorithms implemented at FPGA, the PHA and the DTS based methods, capable of real time processing the fast incoming signals of new GCU LaBr₃ based detectors. While the DTS based algorithm provides improved resolution for the new incoming signals, considered so far the most adequate method to process the GCU data in real time, the PHA is considered the best option to obtain fast results, very useful during test phases. Nevertheless, for final validation further tests are needed, with both horizontal and vertical cameras in place, as well as with plasma. According with past experiments, it is expected an intrinsic noise associated with the long cabling as well as some interference from other systems during JET operation. Moreover, it is known that spectra resolution starts substantially declining when the counting rate increases to a level that the percentage of superimposed pulses becomes essential [13]. Nevertheless, benefiting from the reconfigurable feature of GCU DAQ, FPGA code can always be updated if needed (e.g. improved filtering [10]; advanced pileup resolving methods [14, 15]) without compromising the diagnostic availability.

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